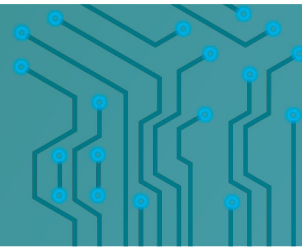


# CONTENTS

IDEC Journal of Integrated Circuits And Systems  
Volume 4 · Number 1 · January 2018



*jicas.idec.or.kr* **JICAS**

## **Design of Low power 4th-Order Delta-Sigma Modulator with single reconfigurable amplifier**

Jae-Hyeon Seong, Dong-Hyun Lee and Kwang-Sup Yoon

–  
**P. 01**

## **Implementation of Power Regulation Unit for Biomedical Implantable Devices**

Sadeque Reza Khan and GoangSeog Choi

–  
**P. 08**

## **A Hardware Implementation of Public-key Cryptographic Processor for 233-bit Elliptic Curve over Binary Field**

Byung-Gwan Park and Kyung-Wook Shin

–  
**P. 14**

## **DC-DC buck converter using hopping technique and capacitor-less LDO regulator design for EMC**

Hai Au Huynh, Soyeon Joo and SoYoung Kim

–  
**P. 20**

## **Design of CMOS Current-reuse LC VCO**

Habib Rastegar, Myeong-U Seong, and Jee Youl Ryu

–  
**P. 27**

## **Design of Hybrid SAR-Cyclic ADC with Gain-error Calibration**

SungHoon Cho and Byung-geun Lee

–  
**P. 32**

## **Time-Domain CMOS Temperature Sensors With Temperature to Digital Converter**

Sanghyung Kim, Hyeondeok Jeon, Chulkyu Park and Joongho Choi

–  
**P. 39**

## **Design of THz Signal Generation Circuits Using 65nm CMOS Technologies**

Hyeong-Jin Kim, Wonseok Choe, and Jinho Jeong

–  
**P. 45**