

# CONTENTS

---

IDEC Journal of Integrated Circuits And Systems  
Volume 5 • Number 1 • January 2019



*jicas.idec.or.kr*

# JICAS

## **Design of a 10-bit SAR A/D Converter with 2-bit/step and Threshold Configuring Comparator**

Ho Yong Lee and Kwang Sub Yoon

—

**P. 02**

---

## **Design of a 2.4 GHz Dual-Mode CMOS Power Amplifier**

Joseph Jang, Jae Yong Lee, Ji Won Kim and Chang Kun Park

—

**P. 08**

---

## **Detection of lattice temperature using gate induced drain leakage in silicon chip**

Nak Won Yoo, Jun Yeon Yun and Young June Park

—

**P. 14**