A Fully-Integrated High-Voltage Generation IC for Implantable Medical Devices
Myeong Gyu Song and Hyouk Kyu Cha

P. 02

A Low-Power 8-bit Switched Capacitor Convolution Engine Optimized for Artificial Neural Networks in 65nm CMOS
Beom Kyu Seo and Jin Tae Kim

P. 08

Implementation of RVDT Signal Conditioner Based-on Costas Loop
Yong Heum Yeon, Seung Won Yang and Jong Yeol Lee

P. 13

Security SoC Architecture with Hardware-Based Pre-Authentication
Won Bae Kong, Pil Joo Choi and Dong Kyue Kim

P. 18

Two-negative feedback loop PLL with frequency voltage converter loop
Dae Hyun Moon and Young Shig Choi

P. 23